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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/824,836	04/04/2001	Kazunori Shionoya	018656-232	1177
7590	11/02/2005		EXAMINER	
Platon N. Mandros BURNS, DOANE, SWECKER & MATHIS, L.L.P. P.O. Box 1404 Alexandria, VA 22313-1404			MILIA, MARK R	
			ART UNIT	PAPER NUMBER
			2622	

DATE MAILED: 11/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

**Advisory Action
Before the Filing of an Appeal Brief**

Application No.

09/824,836

Applicant(s)

SHIONOYA, KAZUNORI

Examiner

Mark R. Milia

Art Unit

2622

--The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

THE REPLY FILED 28 September 2005 FAILS TO PLACE THIS APPLICATION IN CONDITION FOR ALLOWANCE.

1. The reply was filed after a final rejection, but prior to or on the same day as filing a Notice of Appeal. To avoid abandonment of this application, applicant must timely file one of the following replies: (1) an amendment, affidavit, or other evidence, which places the application in condition for allowance; (2) a Notice of Appeal (with appeal fee) in compliance with 37 CFR 41.31; or (3) a Request for Continued Examination (RCE) in compliance with 37 CFR 1.114. The reply must be filed within one of the following time periods:

- a) The period for reply expires 3 months from the mailing date of the final rejection.
- b) The period for reply expires on: (1) the mailing date of this Advisory Action, or (2) the date set forth in the final rejection, whichever is later. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of the final rejection.

Examiner Note: If box 1 is checked, check either box (a) or (b). ONLY CHECK BOX (b) WHEN THE FIRST REPLY WAS FILED WITHIN TWO MONTHS OF THE FINAL REJECTION. See MPEP 706.07(f).

Extensions of time may be obtained under 37 CFR 1.136(a). The date on which the petition under 37 CFR 1.136(a) and the appropriate extension fee have been filed is the date for purposes of determining the period of extension and the corresponding amount of the fee. The appropriate extension fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the shortened statutory period for reply originally set in the final Office action; or (2) as set forth in (b) above, if checked. Any reply received by the Office later than three months after the mailing date of the final rejection, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

NOTICE OF APPEAL

2. The Notice of Appeal was filed on _____. A brief in compliance with 37 CFR 41.37 must be filed within two months of the date of filing the Notice of Appeal (37 CFR 41.37(a)), or any extension thereof (37 CFR 41.37(e)), to avoid dismissal of the appeal. Since a Notice of Appeal has been filed, any reply must be filed within the time period set forth in 37 CFR 41.37(a).

AMENDMENTS

3. The proposed amendment(s) filed after a final rejection, but prior to the date of filing a brief, will not be entered because
 - (a) They raise new issues that would require further consideration and/or search (see NOTE below);
 - (b) They raise the issue of new matter (see NOTE below);
 - (c) They are not deemed to place the application in better form for appeal by materially reducing or simplifying the issues for appeal; and/or
 - (d) They present additional claims without canceling a corresponding number of finally rejected claims.

NOTE: _____. (See 37 CFR 1.116 and 41.33(a)).

4. The amendments are not in compliance with 37 CFR 1.121. See attached Notice of Non-Compliant Amendment (PTOL-324).
5. Applicant's reply has overcome the following rejection(s): _____.
6. Newly proposed or amended claim(s) _____ would be allowable if submitted in a separate, timely filed amendment canceling the non-allowable claim(s).
7. For purposes of appeal, the proposed amendment(s): a) will not be entered, or b) will be entered and an explanation of how the new or amended claims would be rejected is provided below or appended.

The status of the claim(s) is (or will be) as follows:

Claim(s) allowed: _____

Claim(s) objected to: _____

Claim(s) rejected: _____

Claim(s) withdrawn from consideration: _____

AFFIDAVIT OR OTHER EVIDENCE

8. The affidavit or other evidence filed after a final action, but before or on the date of filing a Notice of Appeal will not be entered because applicant failed to provide a showing of good and sufficient reasons why the affidavit or other evidence is necessary and was not earlier presented. See 37 CFR 1.116(e).
9. The affidavit or other evidence filed after the date of filing a Notice of Appeal, but prior to the date of filing a brief, will not be entered because the affidavit or other evidence failed to overcome all rejections under appeal and/or appellant fails to provide a showing a good and sufficient reasons why it is necessary and was not earlier presented. See 37 CFR 41.33(d)(1).
10. The affidavit or other evidence is entered. An explanation of the status of the claims after entry is below or attached.

REQUEST FOR RECONSIDERATION/OTHER

11. The request for reconsideration has been considered but does NOT place the application in condition for allowance because:
See attached explanation.
12. Note the attached Information Disclosure Statement(s). (PTO/SB/08 or PTO-1449) Paper No(s). _____
13. Other: _____

DETAILED ACTION

Response to Amendment

1. Applicant's amendment/remarks was received on 9/28/05 and has been entered and made of record.

Response to Arguments

2. Applicant's arguments filed 9/28/05 have been fully considered but they are not persuasive.
3. In response to applicant's arguments regarding the rejection of claims 1-9, more specifically claims 1, 4, and 7, wherein on pages 1-2, applicant asserts that the reference of Ho does not disclose a "printed wiring board" or a "memory board". The examiner respectfully disagrees as the reference of Ho does disclose such features. Particularly, Ho states that the expansion memory sockets can be located in a computer to which it is well known that expansion memory boards are inserted into a larger printed circuit board containing the base memory and other processing components (see column 3 lines 29-34). Also, Ho states that the base memory and the expansion memory are physically associated with the central processing unit (CPU) (see column 3 lines 37-39). In addition, Ho uses the terms module and board interchangeably throughout the description of the invention. It can be seen from column 3 lines 27-40

that the expansion memory boards are inserted into module sockets, or board sockets, of the memory subsystem "10", which in a computer system is inherently a printed circuit board. Further, the expansion memory boards are printed circuit boards (see column 1 lines 19-23 and column 3 lines 16-20) and as such are inserted into a socket that is part of a larger printed circuit board, as seen in conventional computer systems and as set forth in the reference of Ho. Additionally, the memory subsystem "10" also comprises a programmable logic device (see column 3 lines 40-47), which is known in the art to be an integrated circuit, a chip of some sort, that can be connected to a printed circuit board to aid in the performance of complex functions. Therefore, it can be seen from the above evidence that the memory subsystem "10" is a printed wiring board containing expansion memory sockets to which expansion memory boards can be inserted to increase memory capacity.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mark R. Milia whose telephone number is (571) 272-7408. The examiner can normally be reached M-F 8:00am-4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Coles can be reached at (571) 272-7402. The fax number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mark R. Milia
Examiner
Art Unit 2622

MRM


EDWARD COLES
SUPPLYING PATENT EXAMINER
TECHNOLOGY CENTER 2600